

FIG. 1

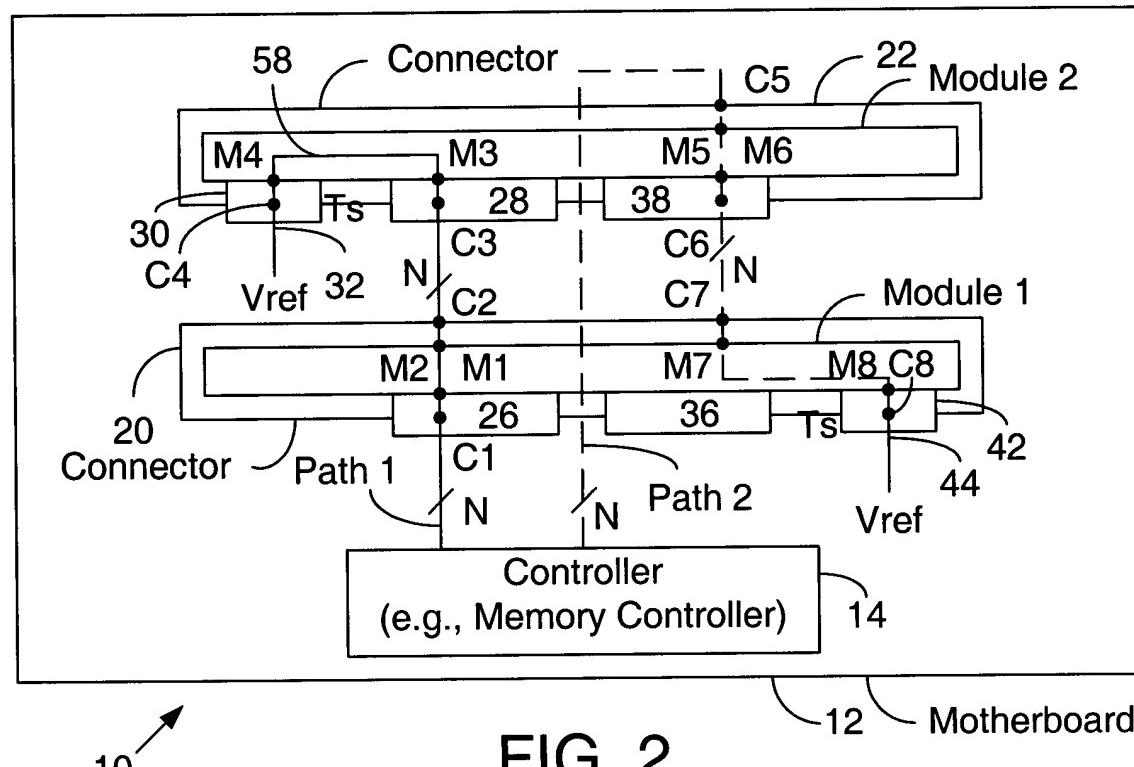


FIG. 2

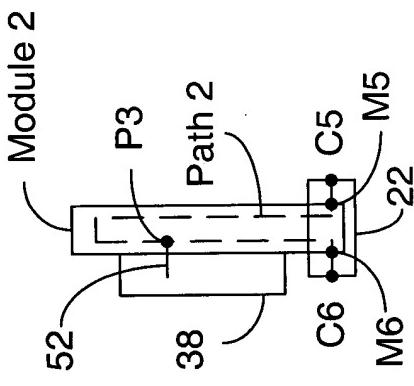
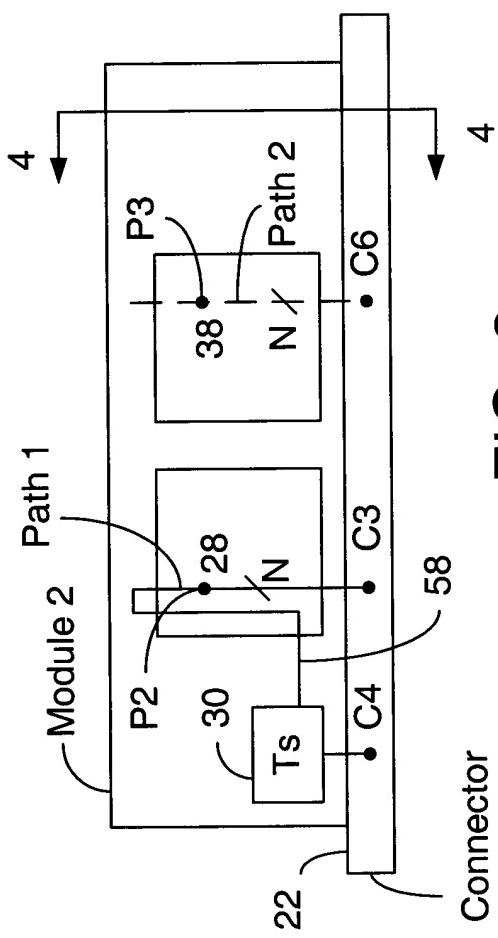


FIG. 4

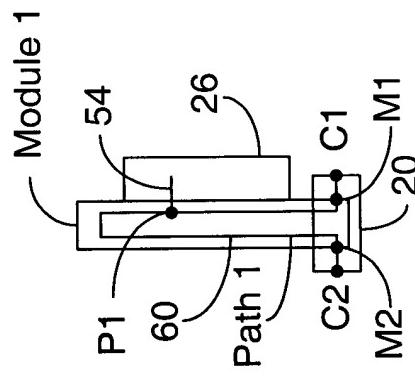
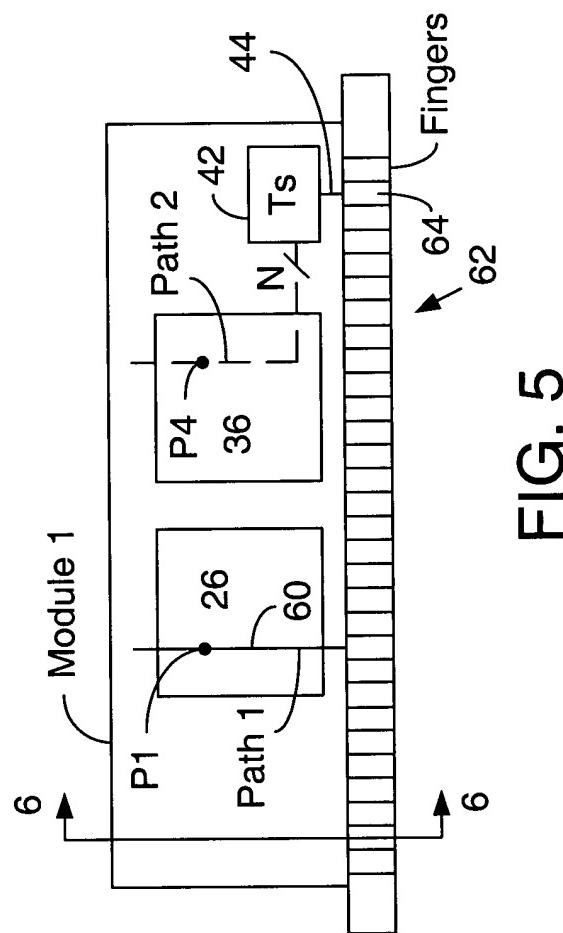


FIG. 6

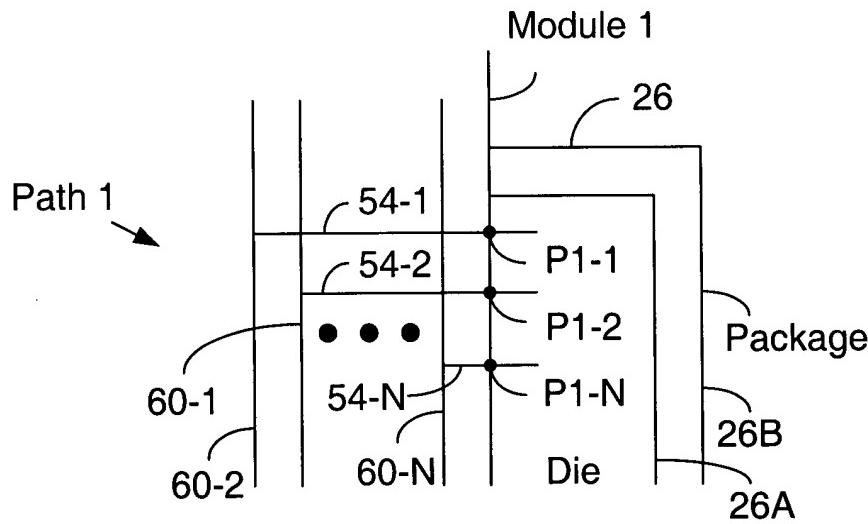


FIG. 7

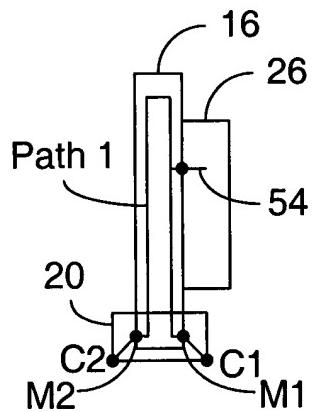


FIG. 8

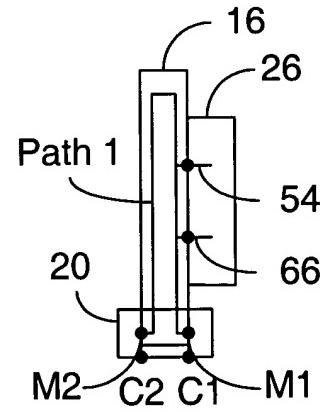


FIG. 9

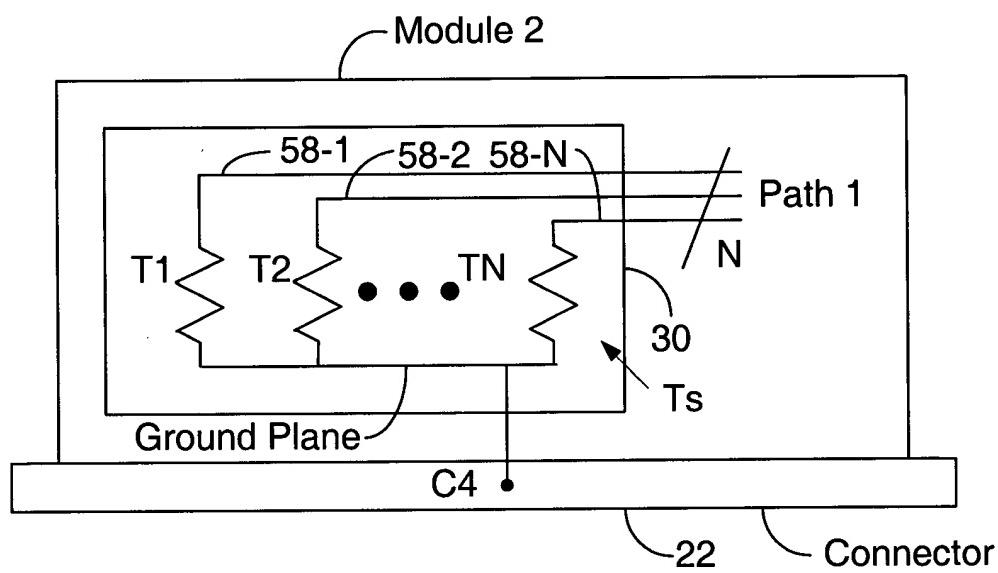


FIG. 10

90

FIG. 11

Motherboard

94

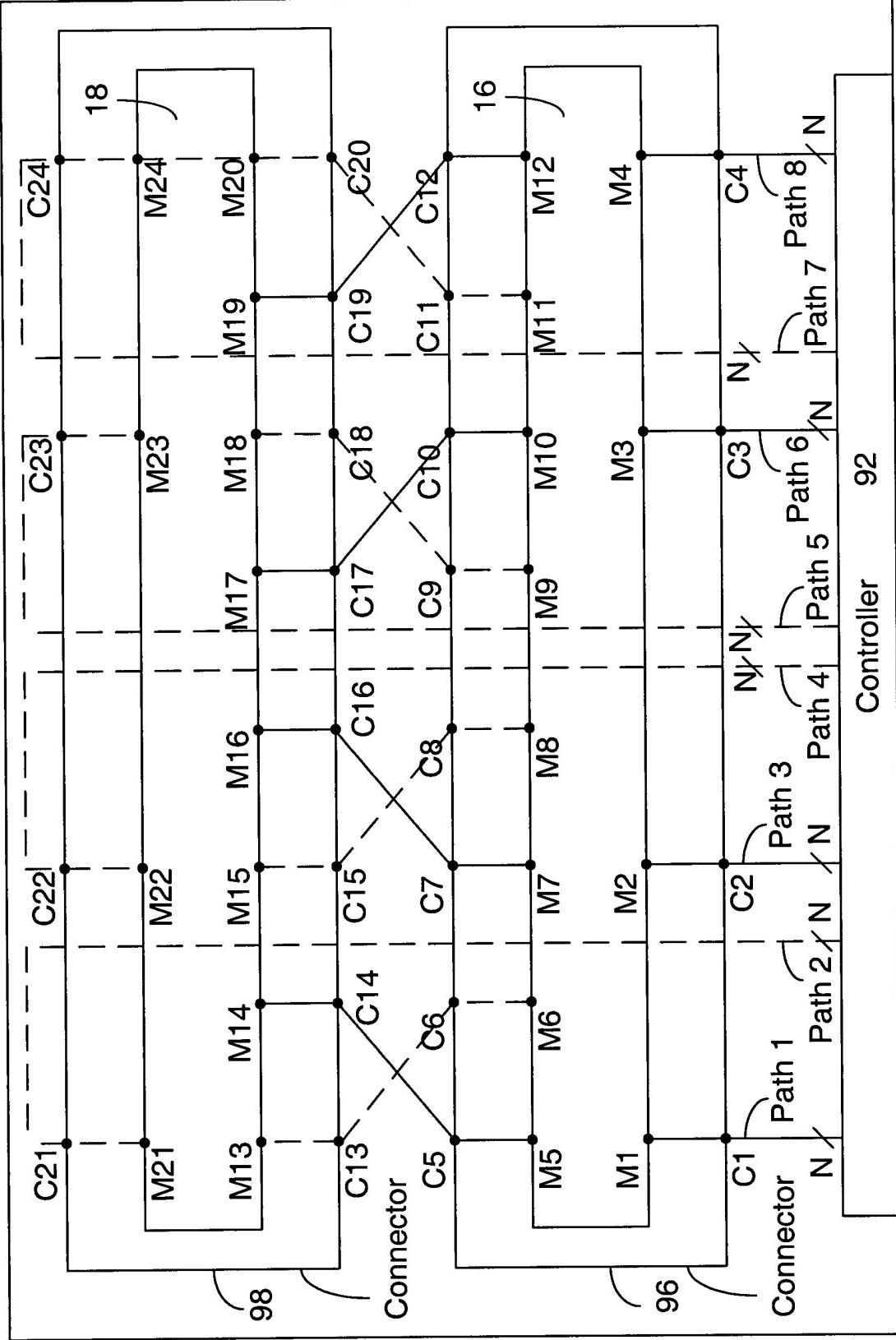
Controller 92

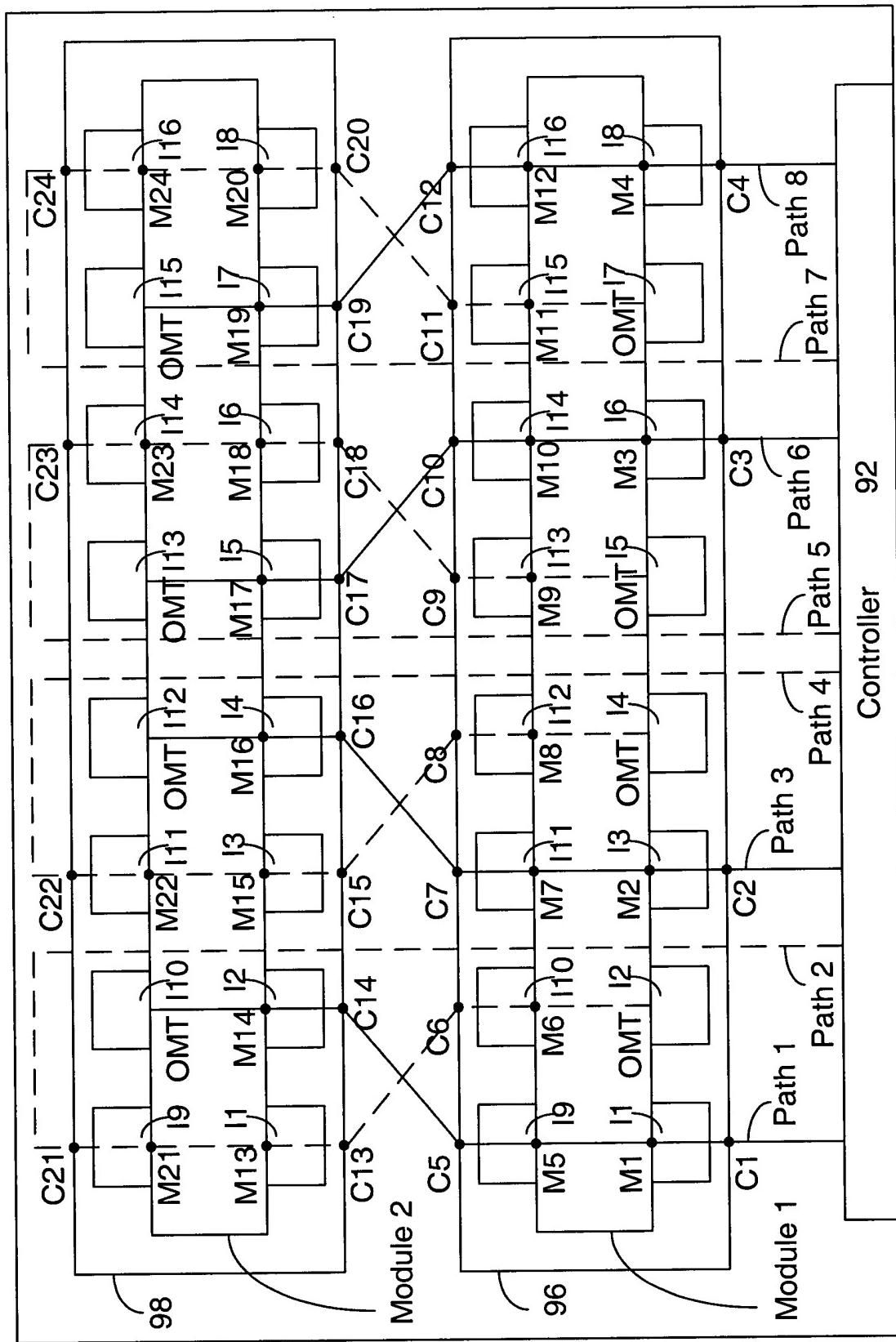
C4
Path 8
N
Path 7
N
Path 6
N
Path 5
N
Path 4
N
Path 3
N
C2
Path 1
N
C1
Connector
96

M12
M11
M10
M9
M8
M7
M6
M5
M4
M3
M2
M1

M20
M19
M18
M17
M16
M15
M14
M22
M21

C20
C19
C18
C17
C16
C15
C14
C13
C12
C11
C10
C9
C8
C7
C6
C5
Connector
98

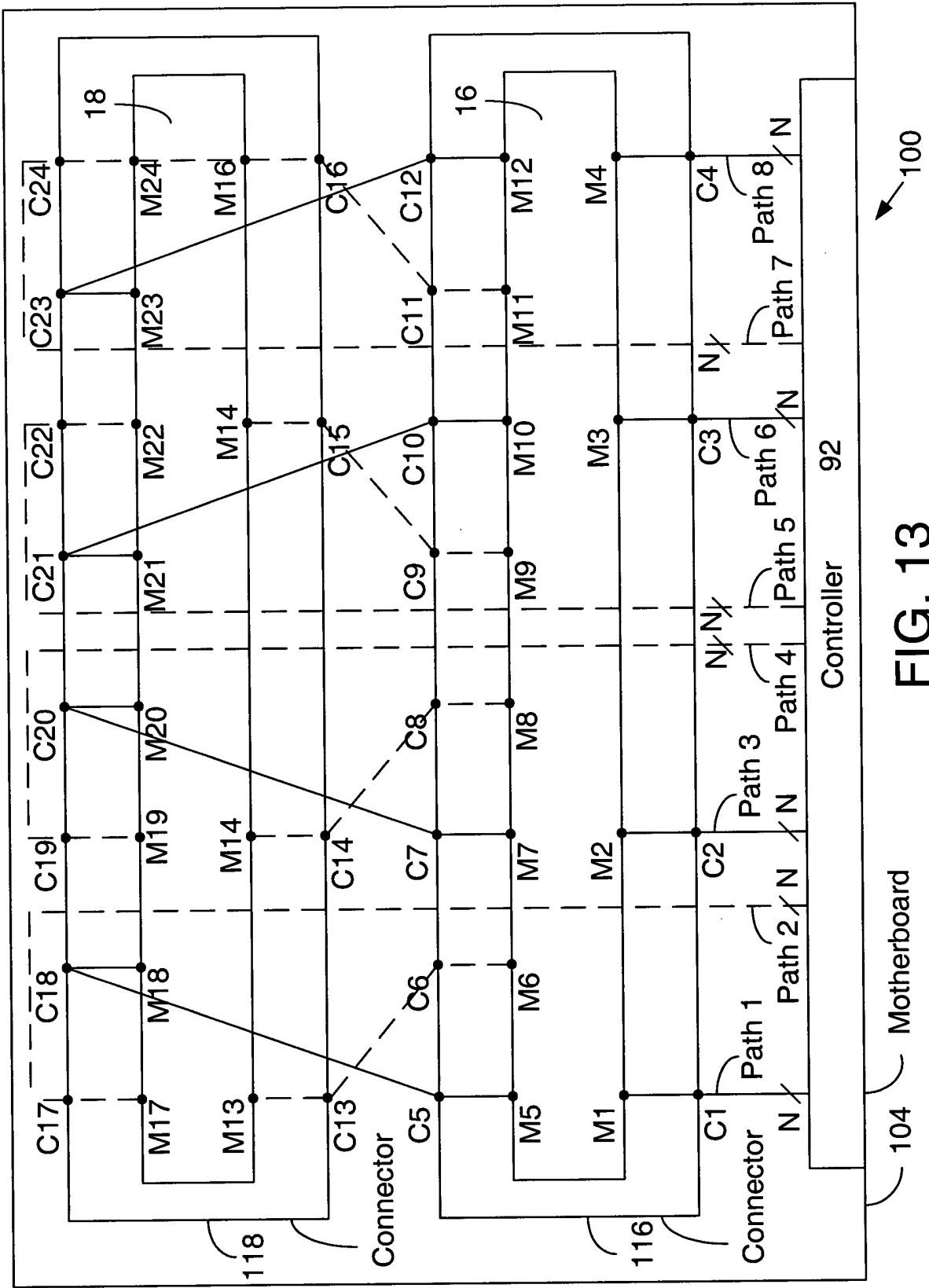




94 Motherboard

90
FIG. 12

92



100

FIG. 13

Motherboard

Controller 92

104

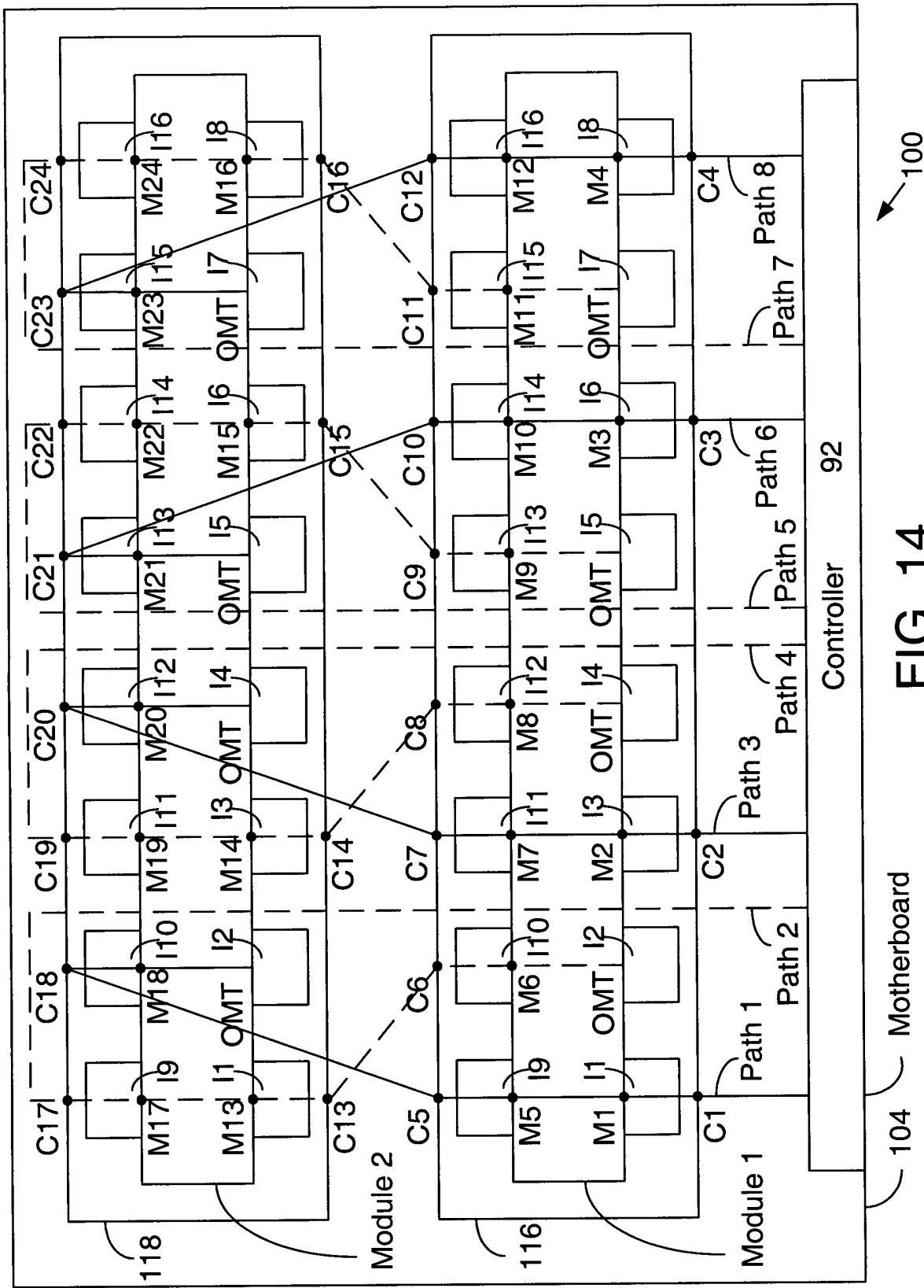


FIG. 14

104 Motherboard

100

Controller 92

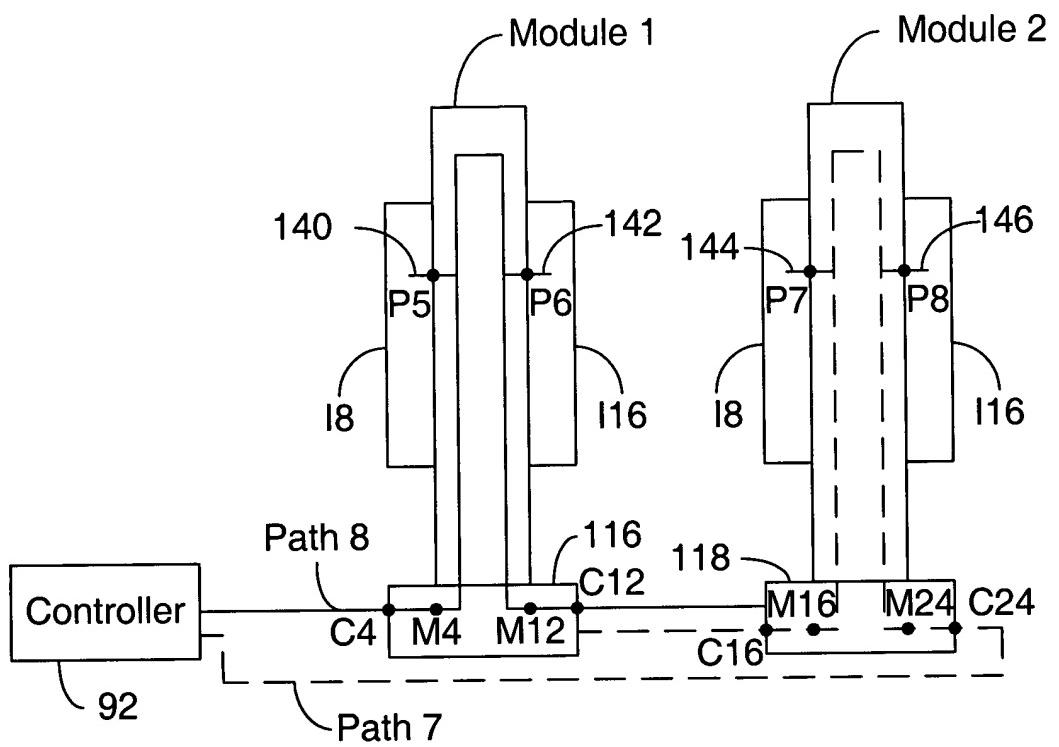


FIG. 15

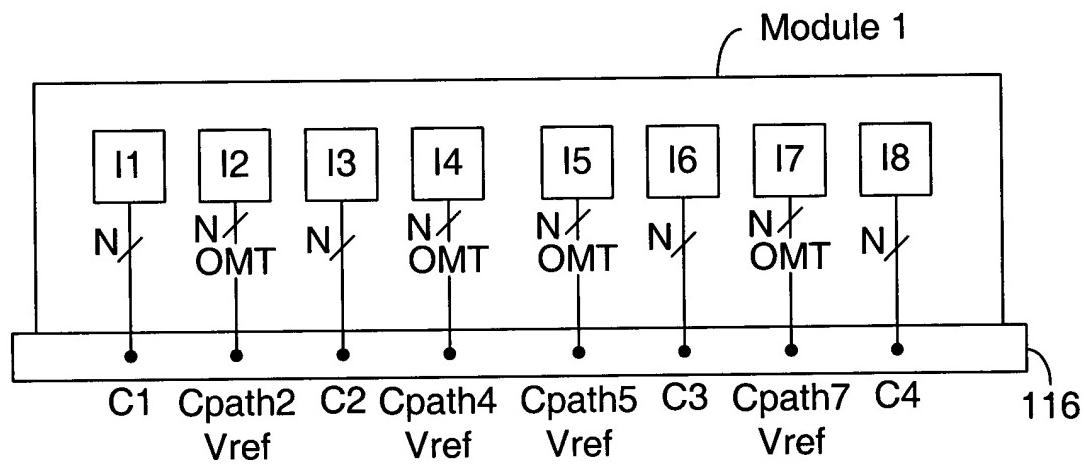


FIG. 16

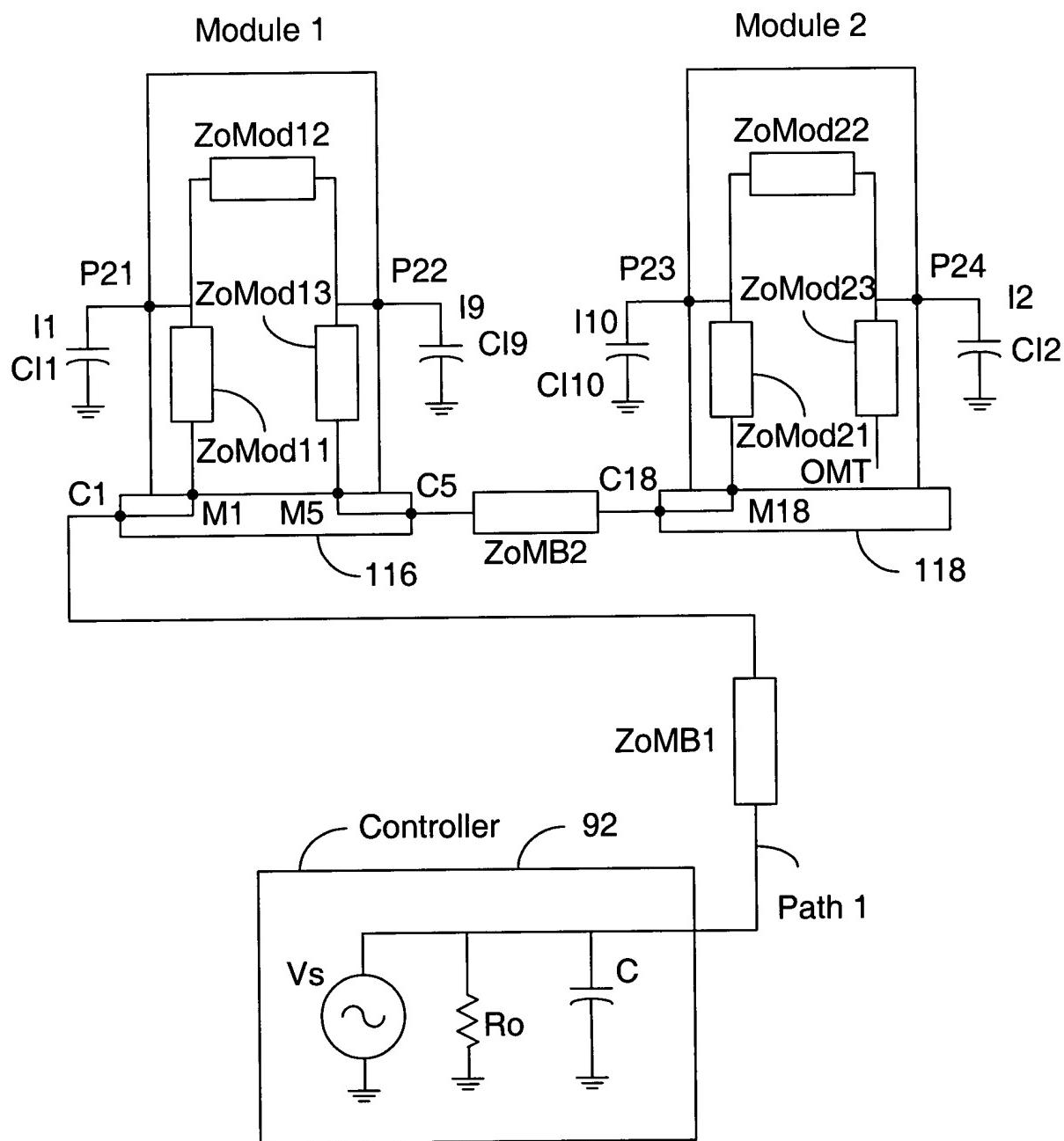


FIG. 17

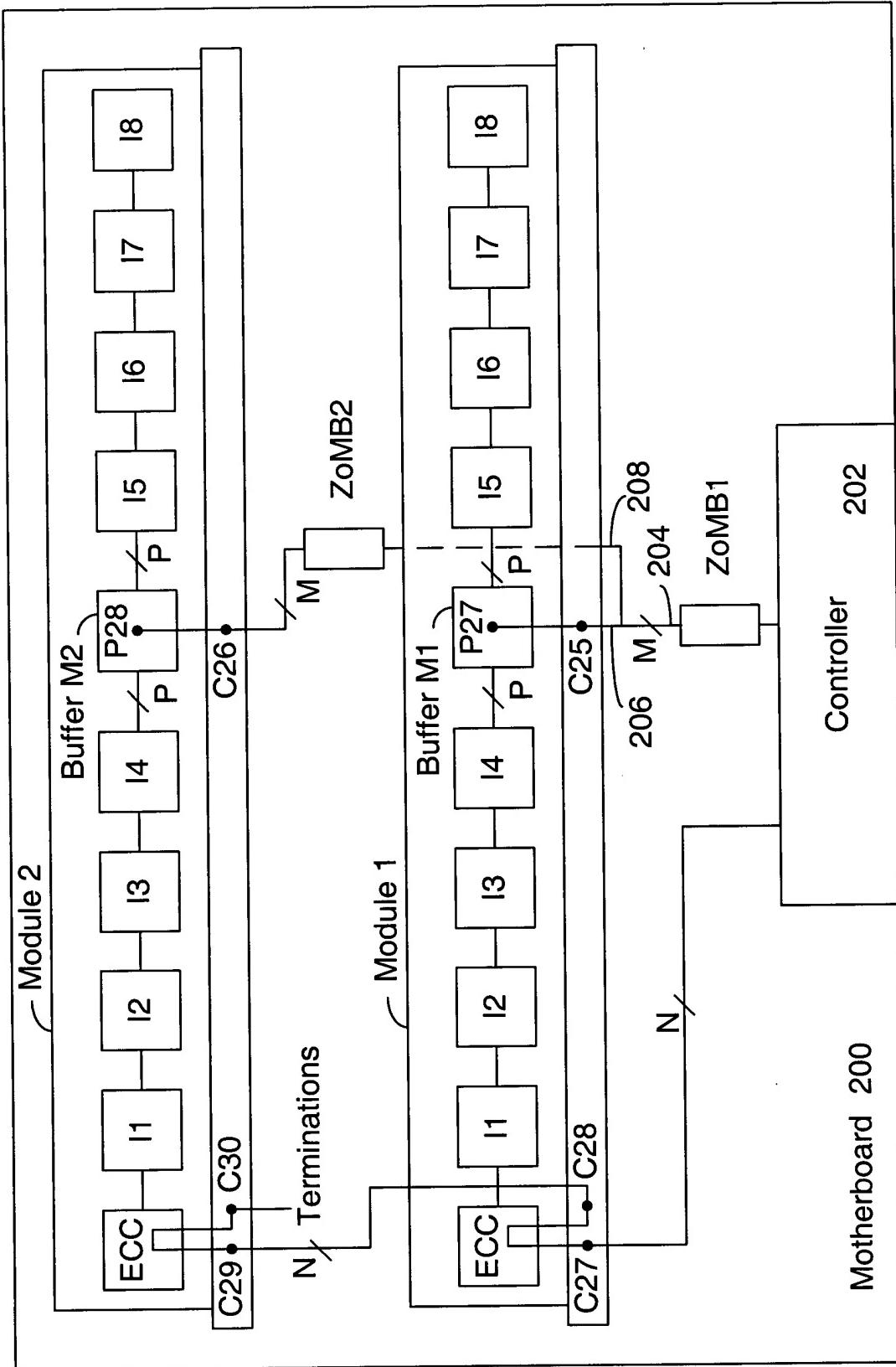


FIG. 18

FIG. 19

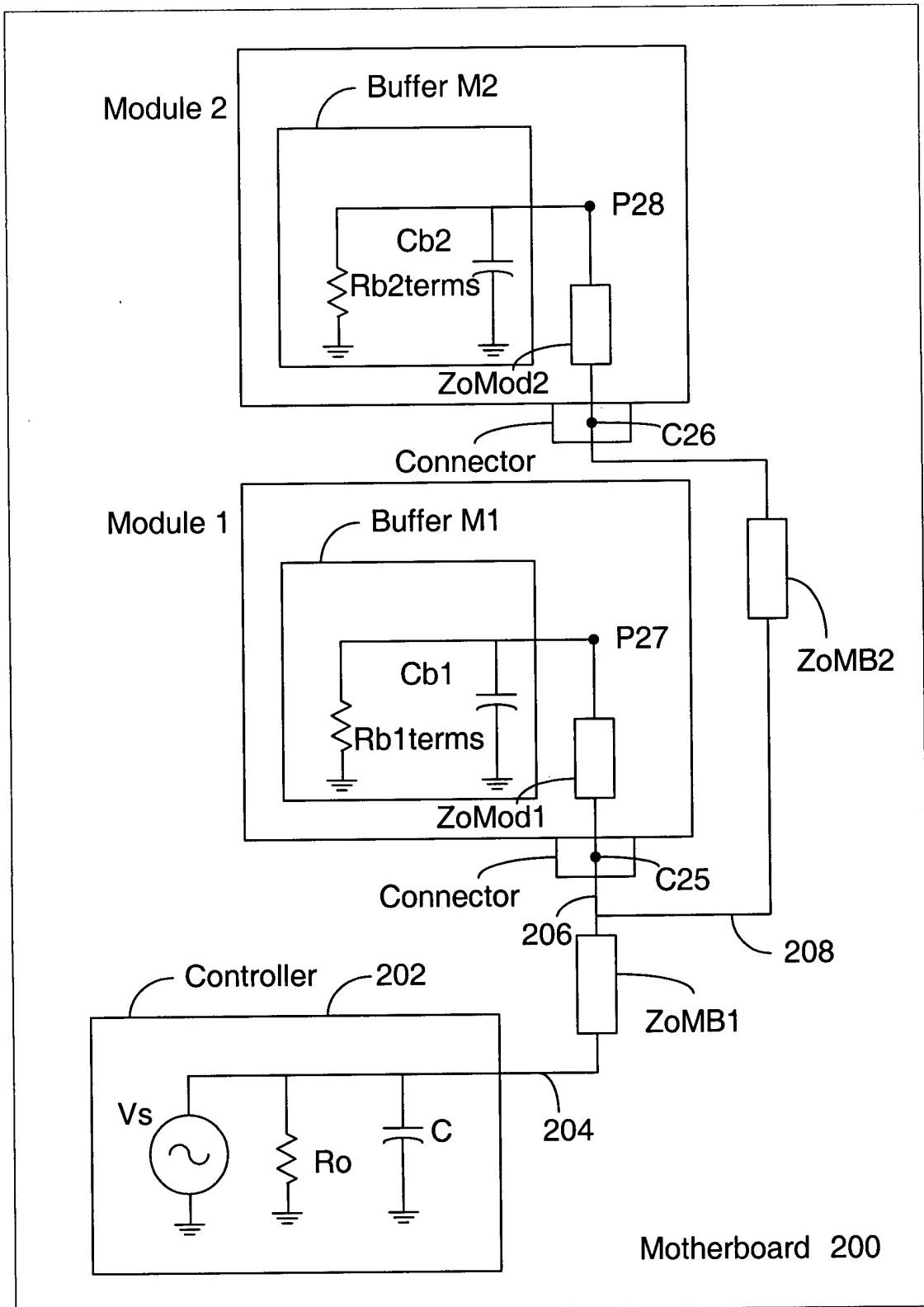


FIG. 19

7.05520 = 3537160

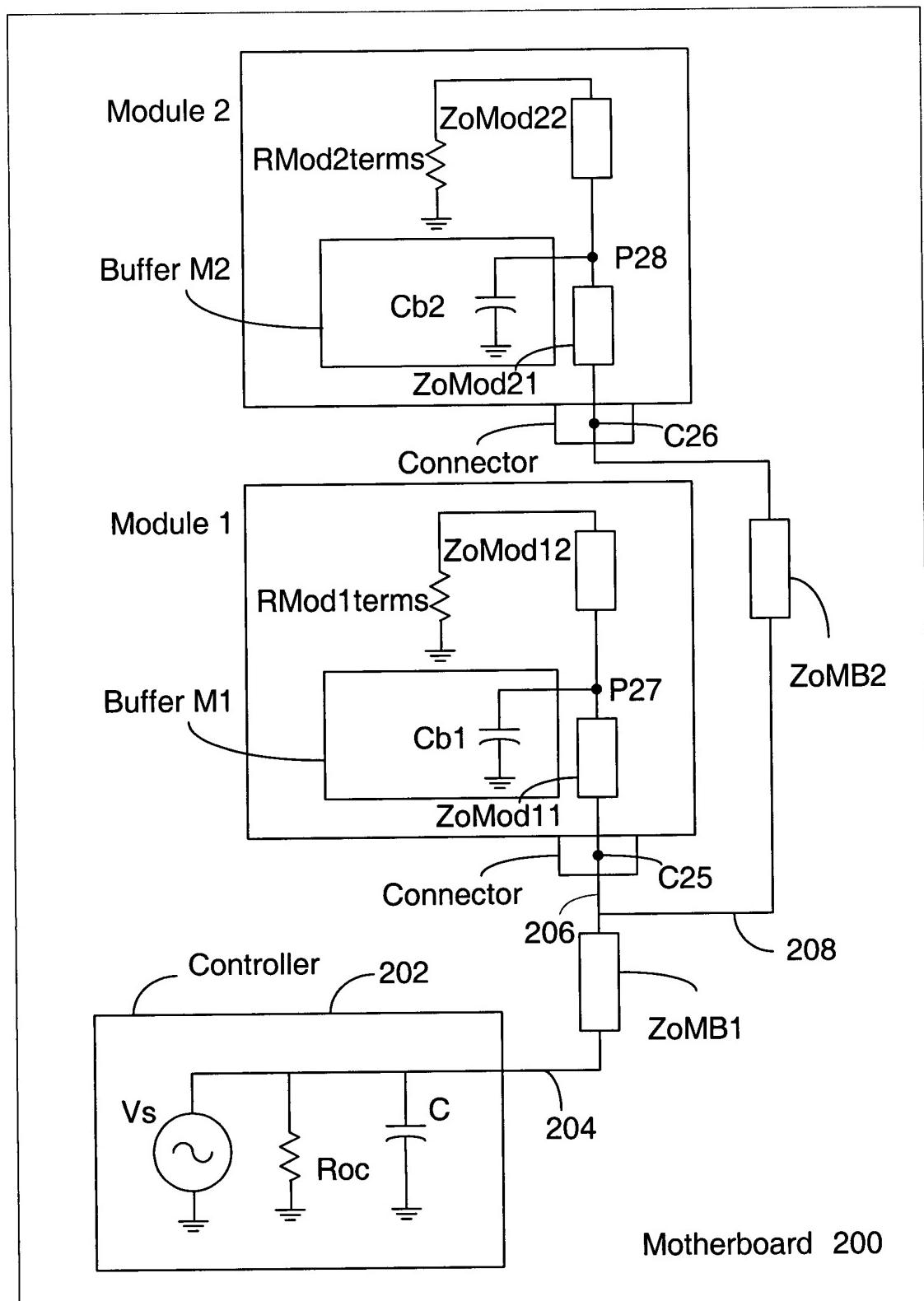


FIG. 20

TOP SECRET//COMINT

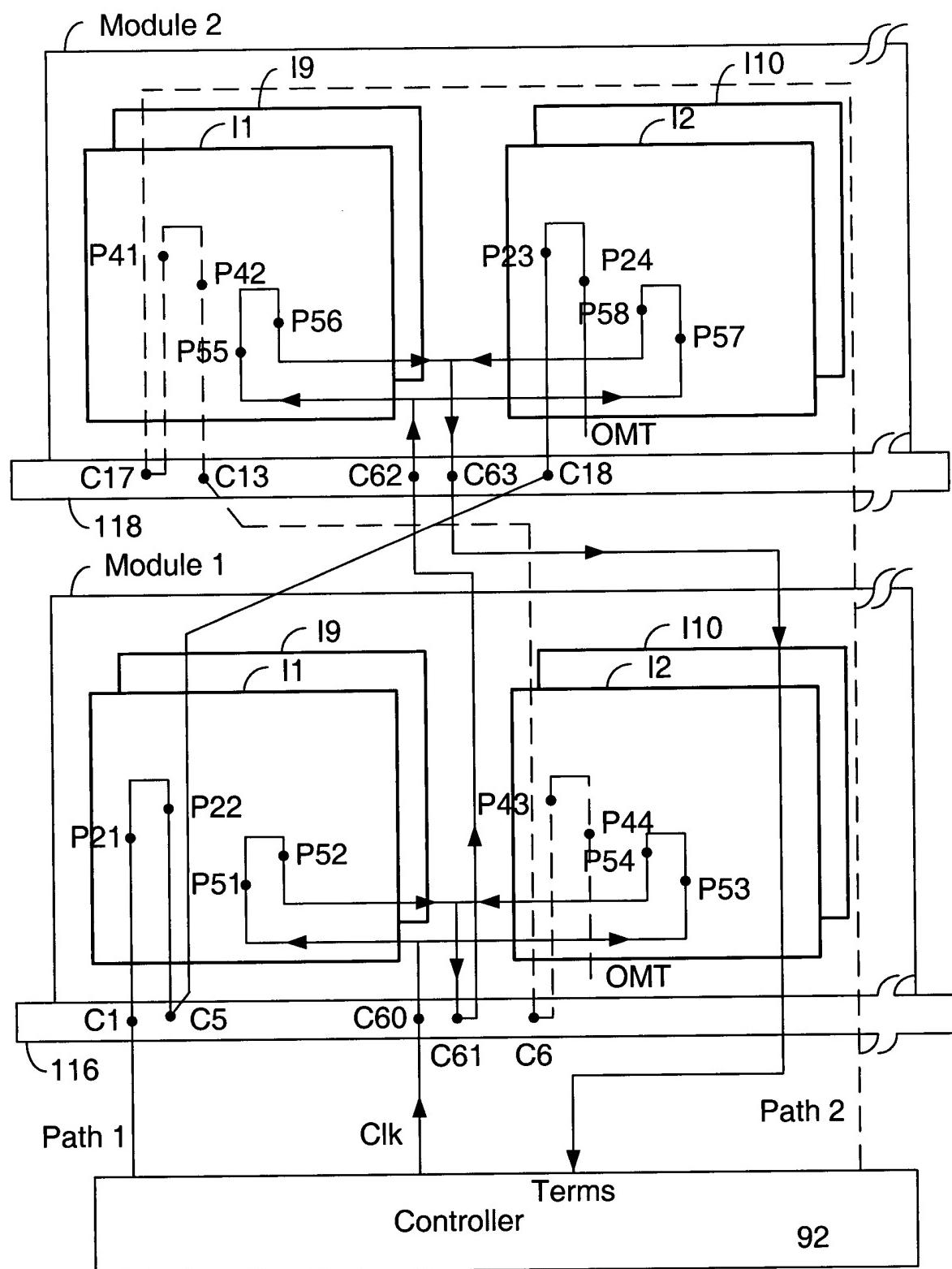


FIG. 21

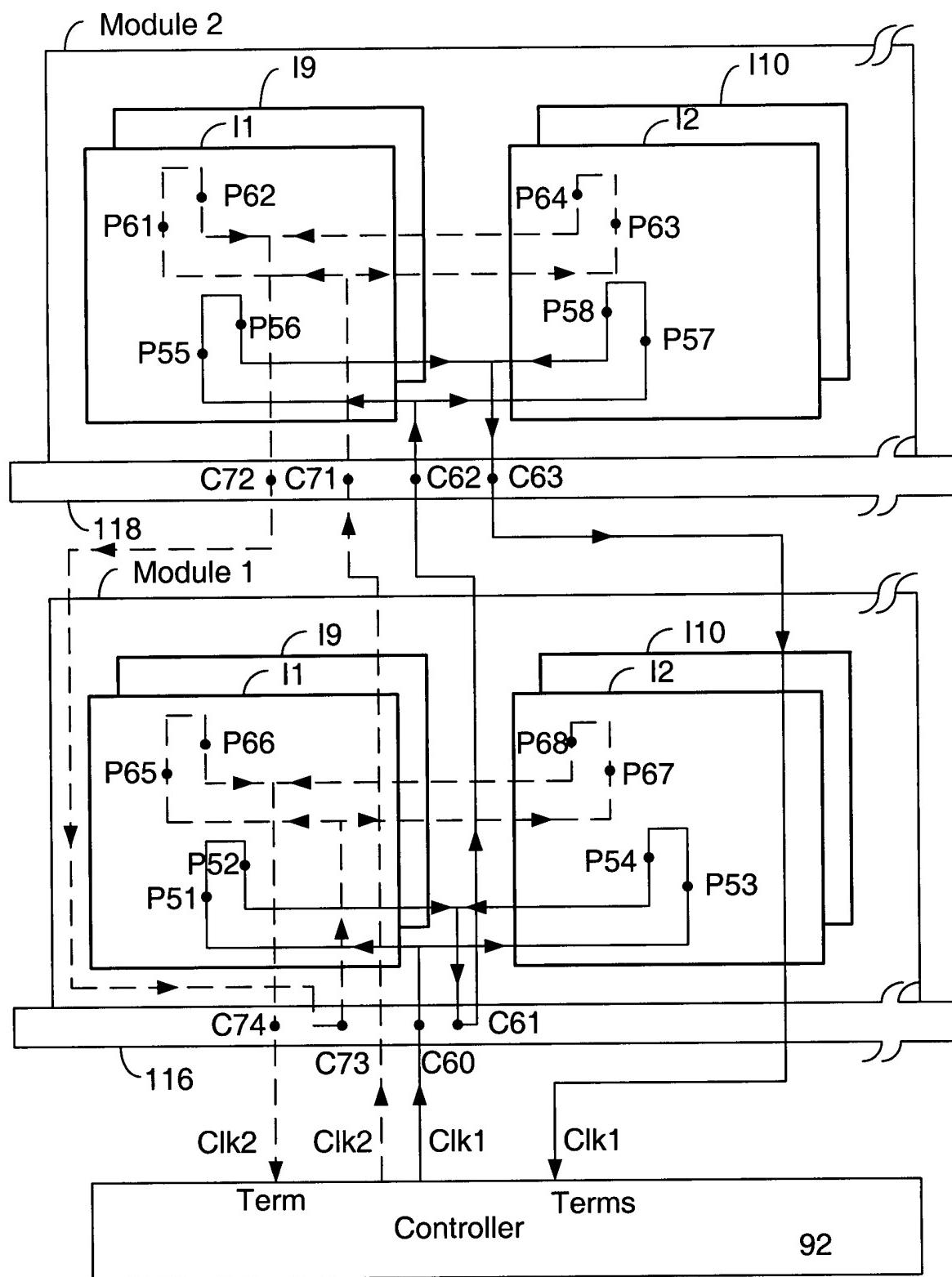


FIG. 22

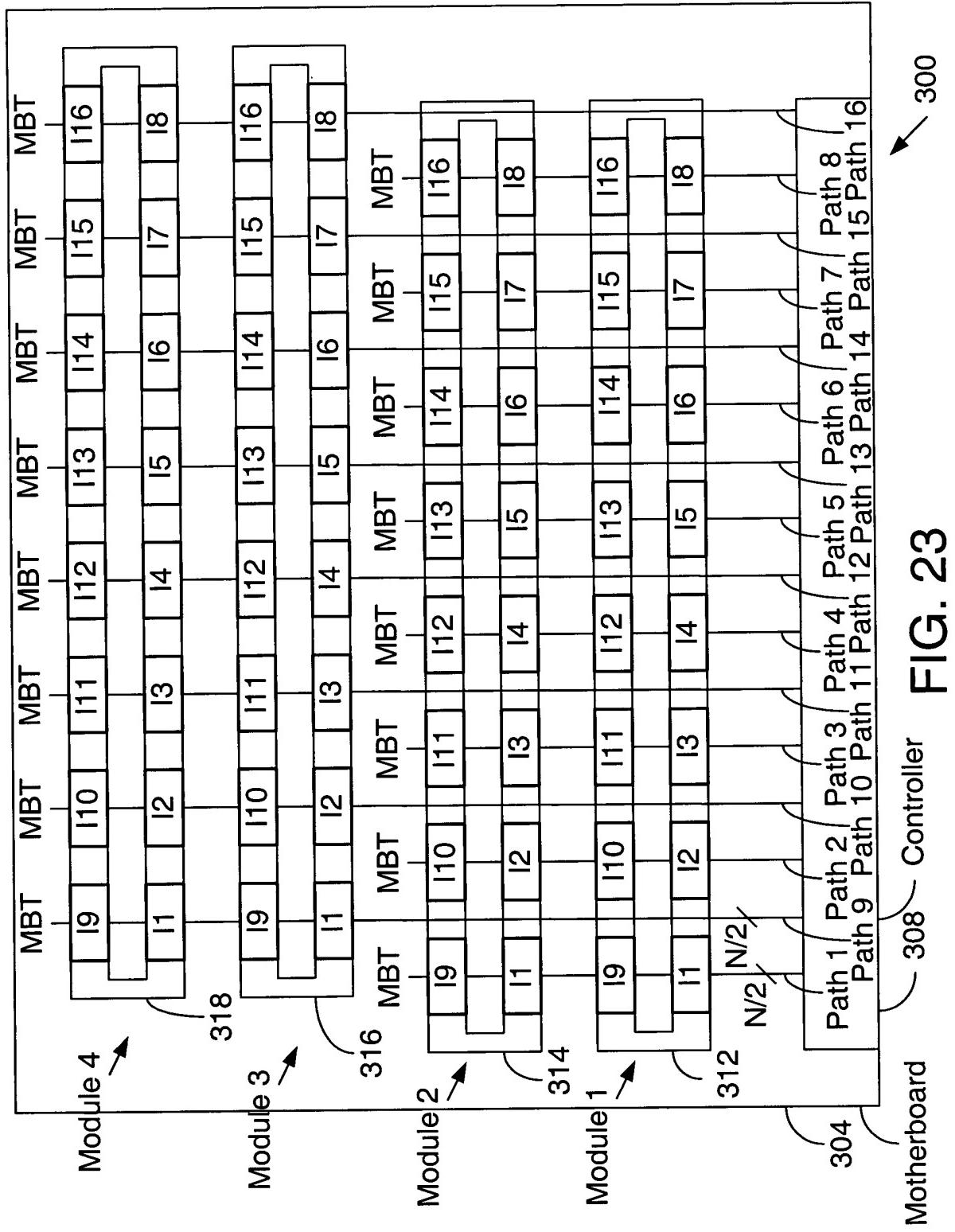


FIG. 23

300

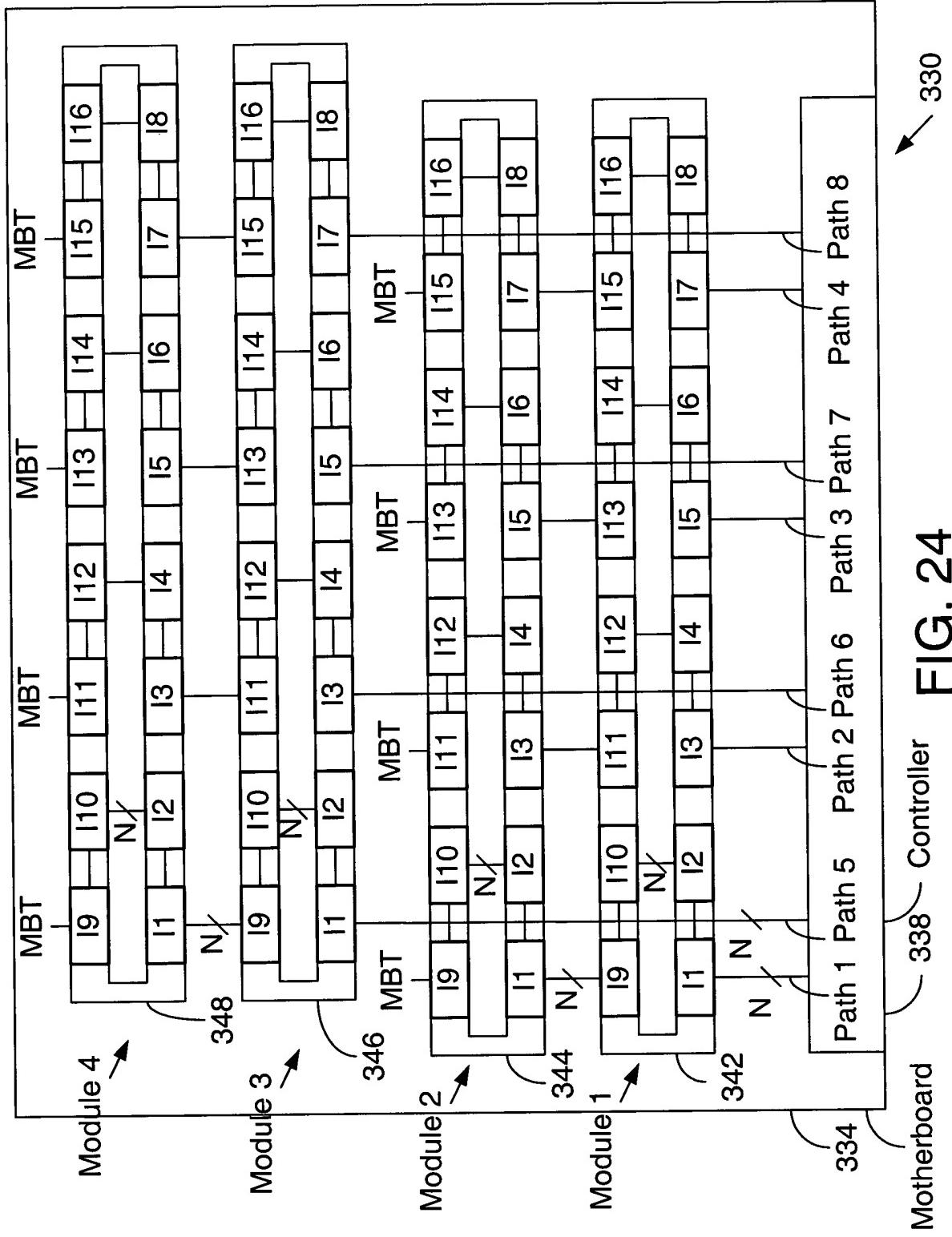


FIG. 24

330

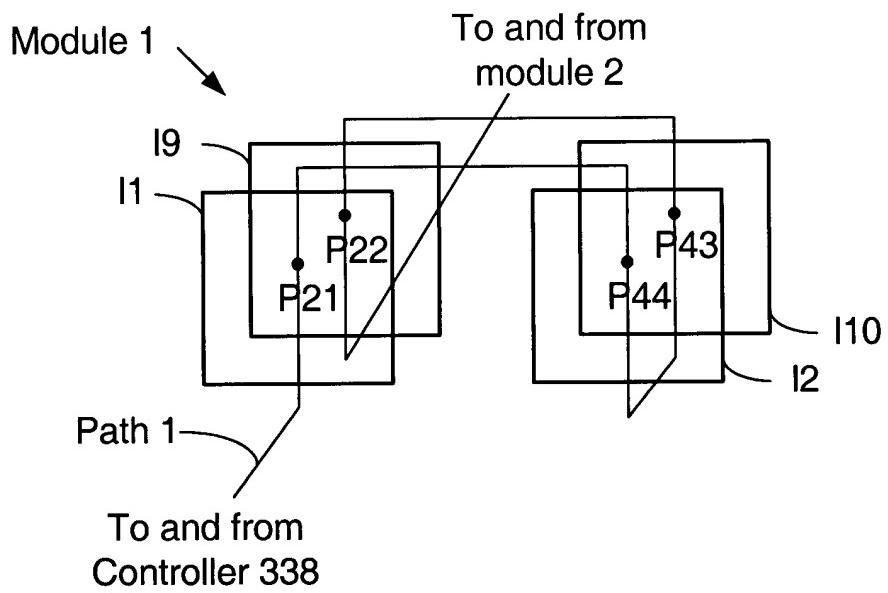


FIG. 25

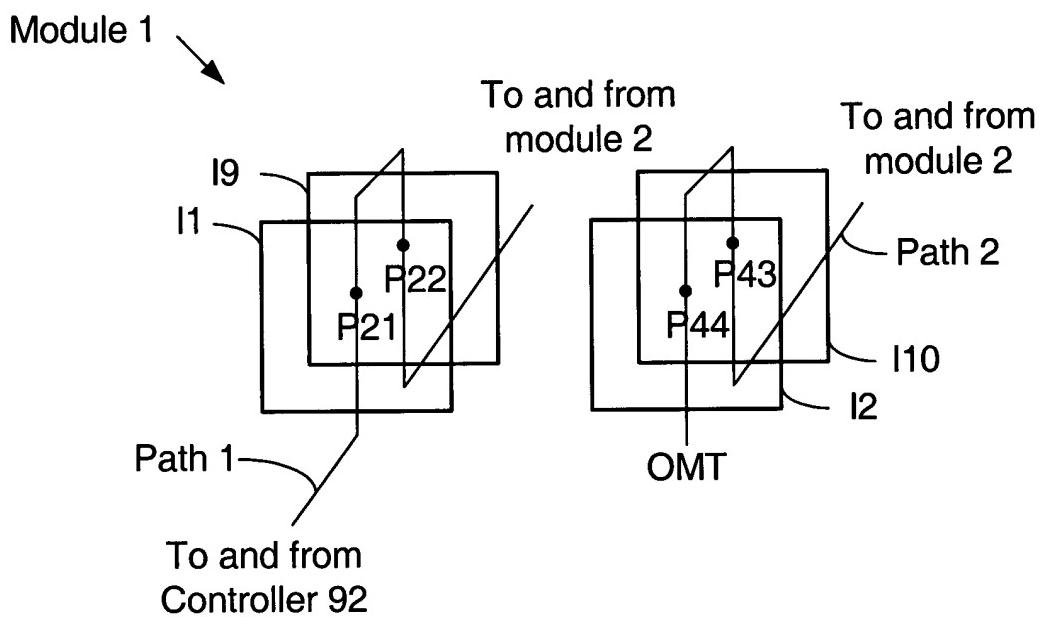


FIG. 26

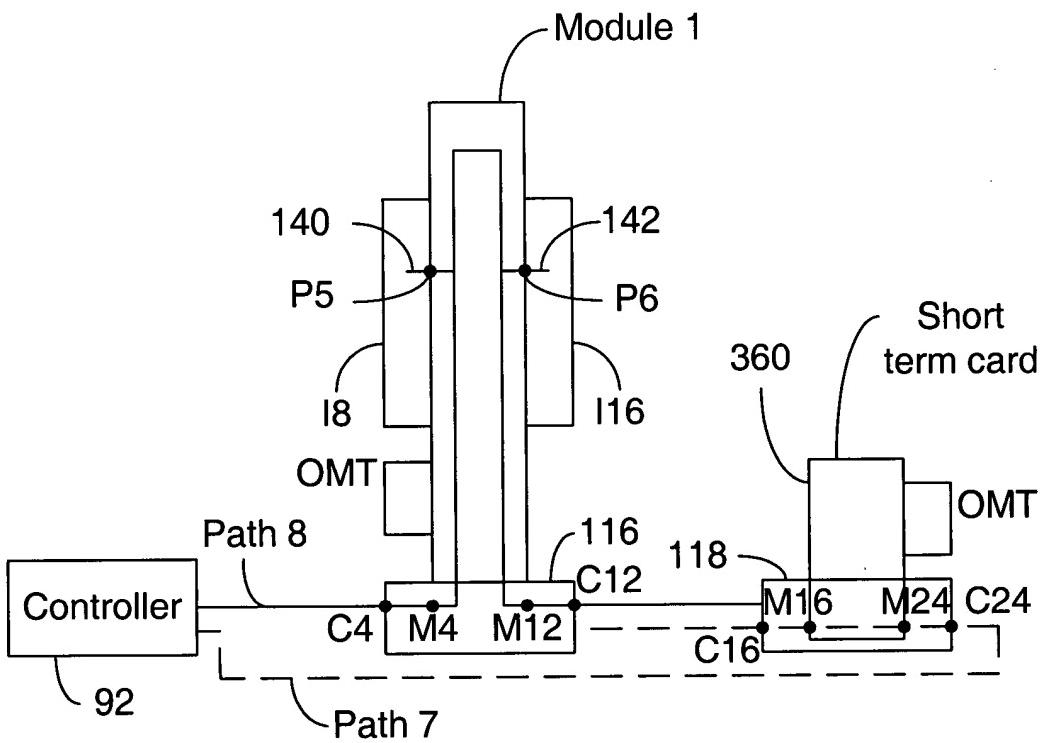


FIG. 27

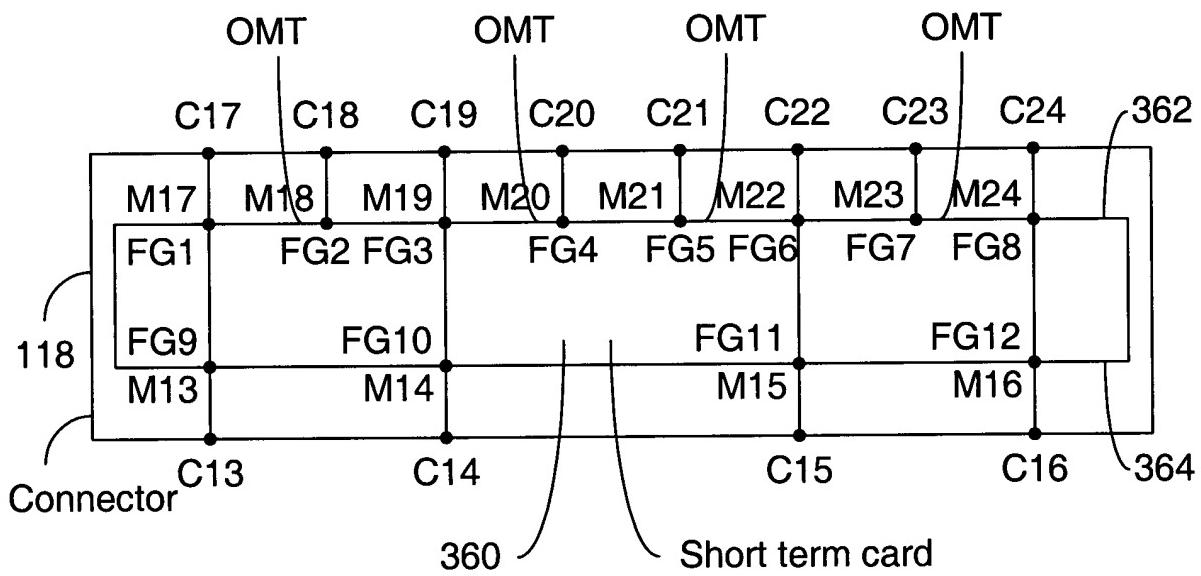
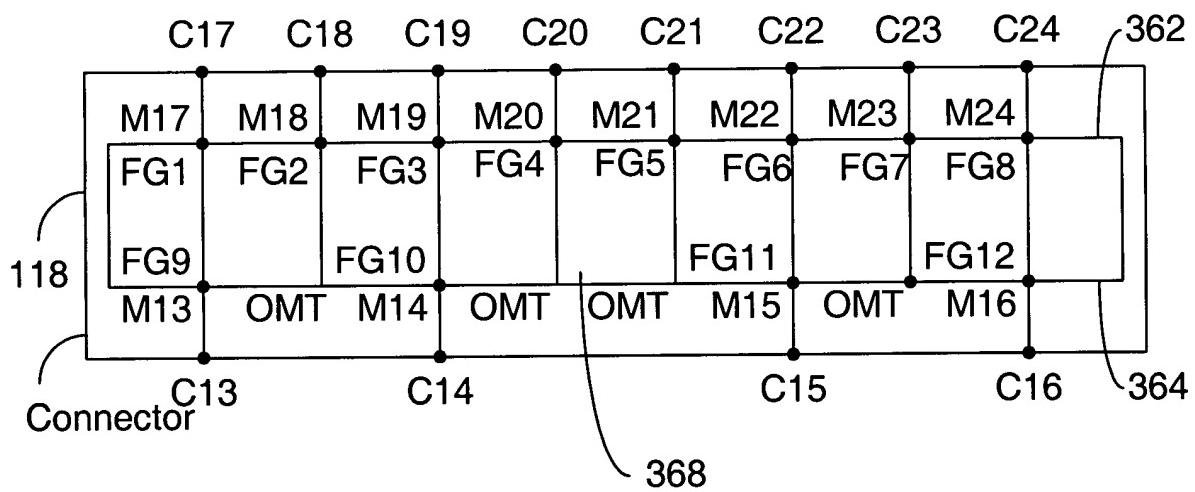


FIG. 28



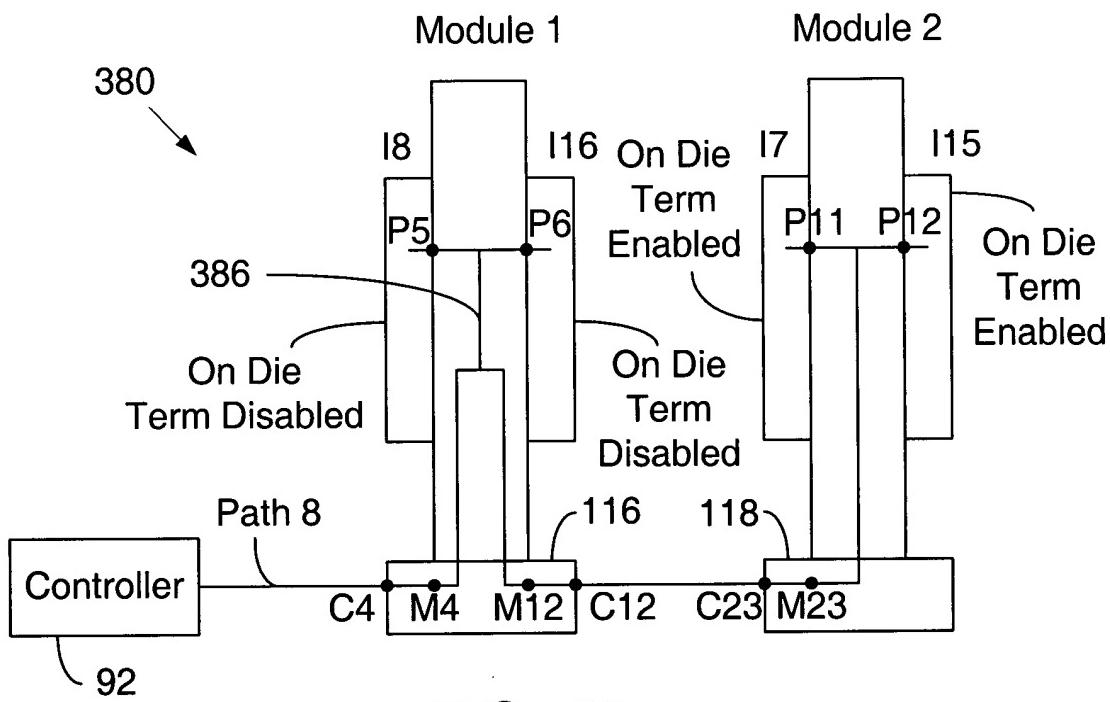


FIG. 30

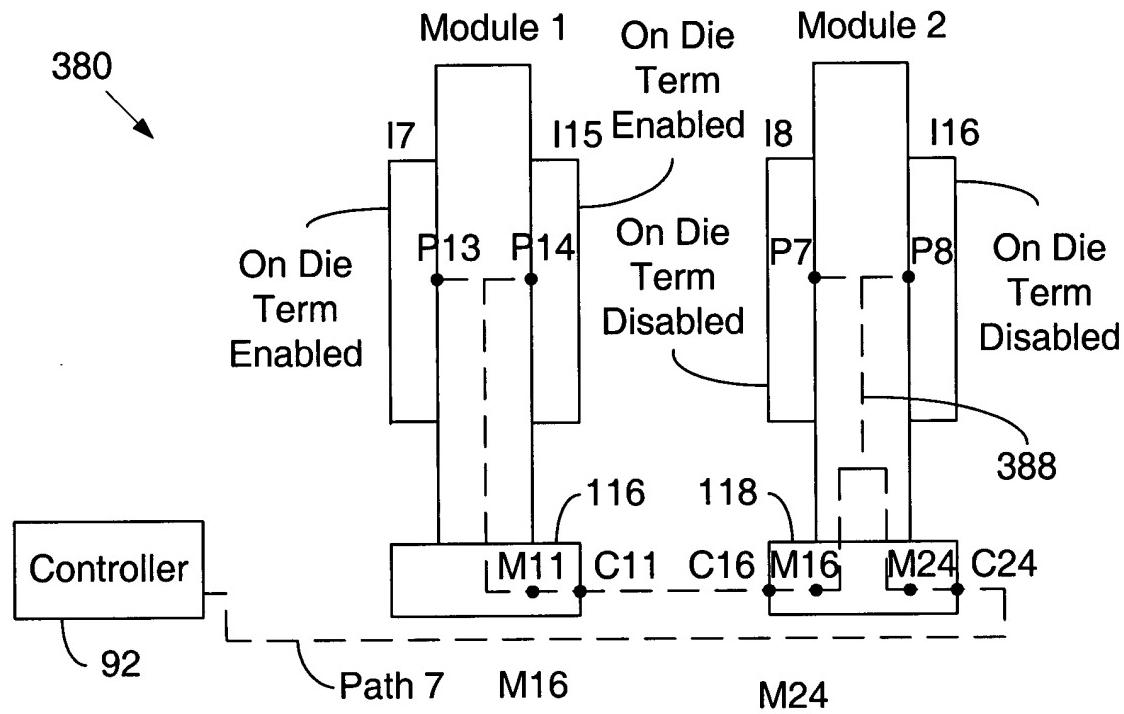


FIG. 31

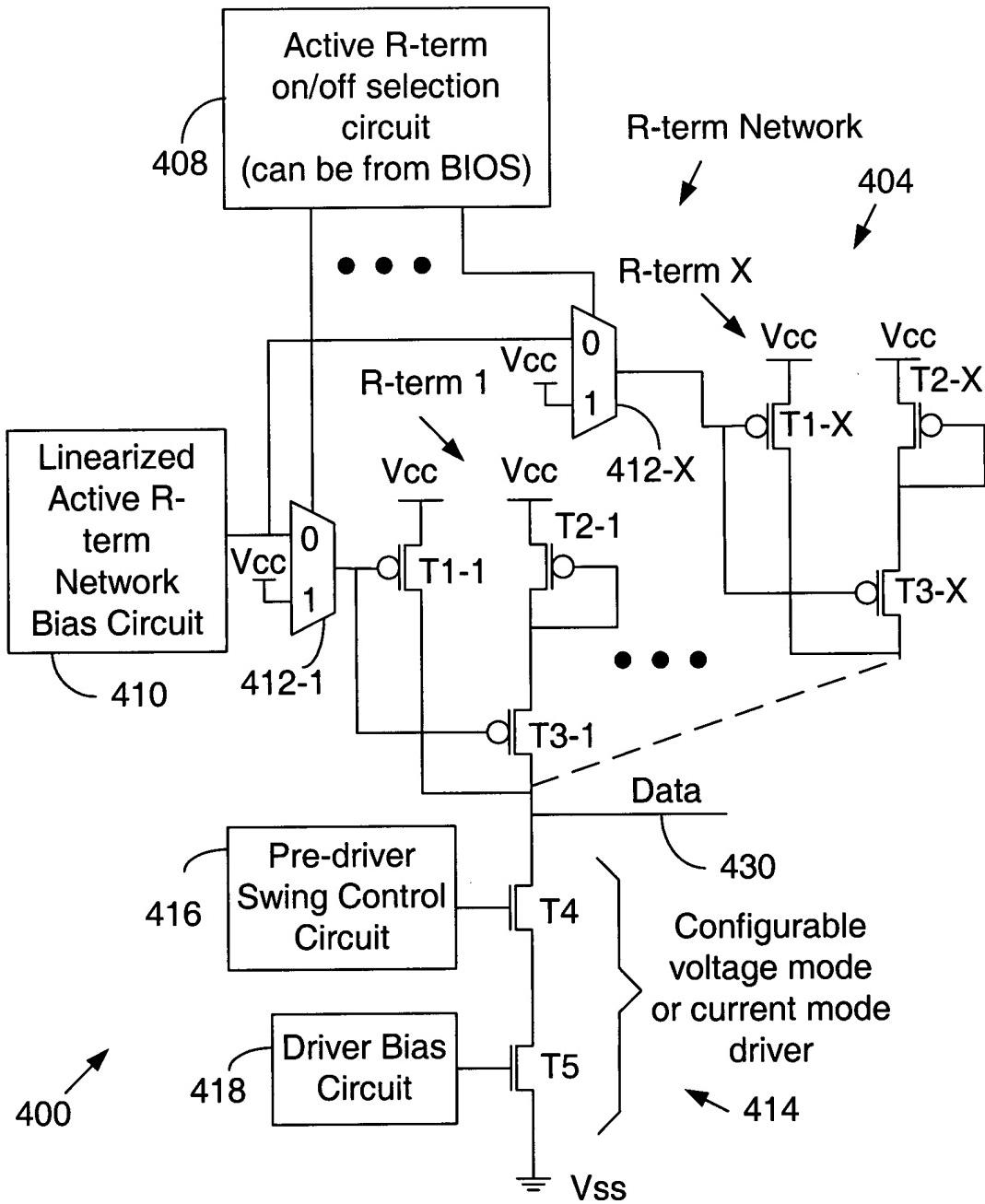


FIG. 32

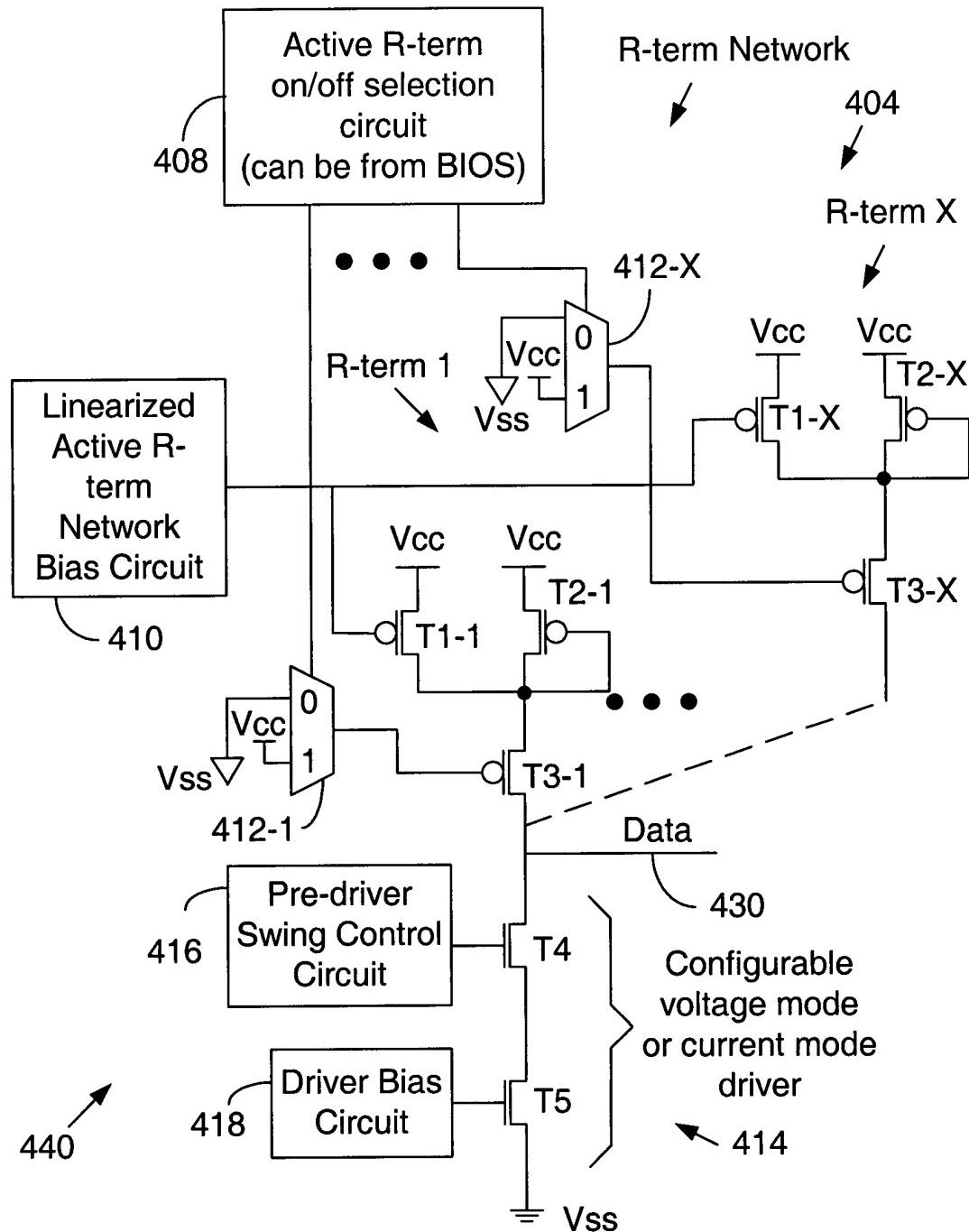


FIG. 33

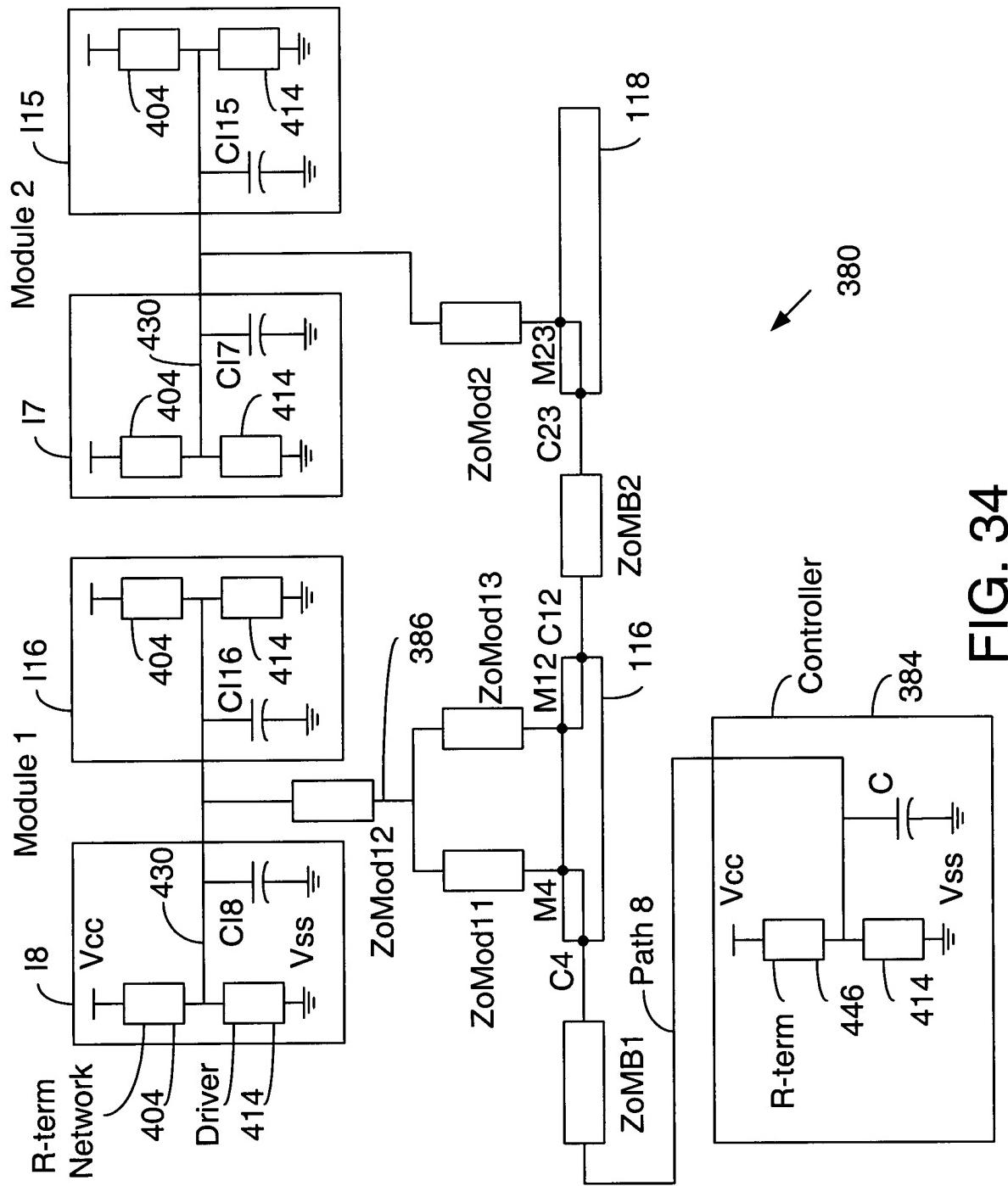


FIG. 34

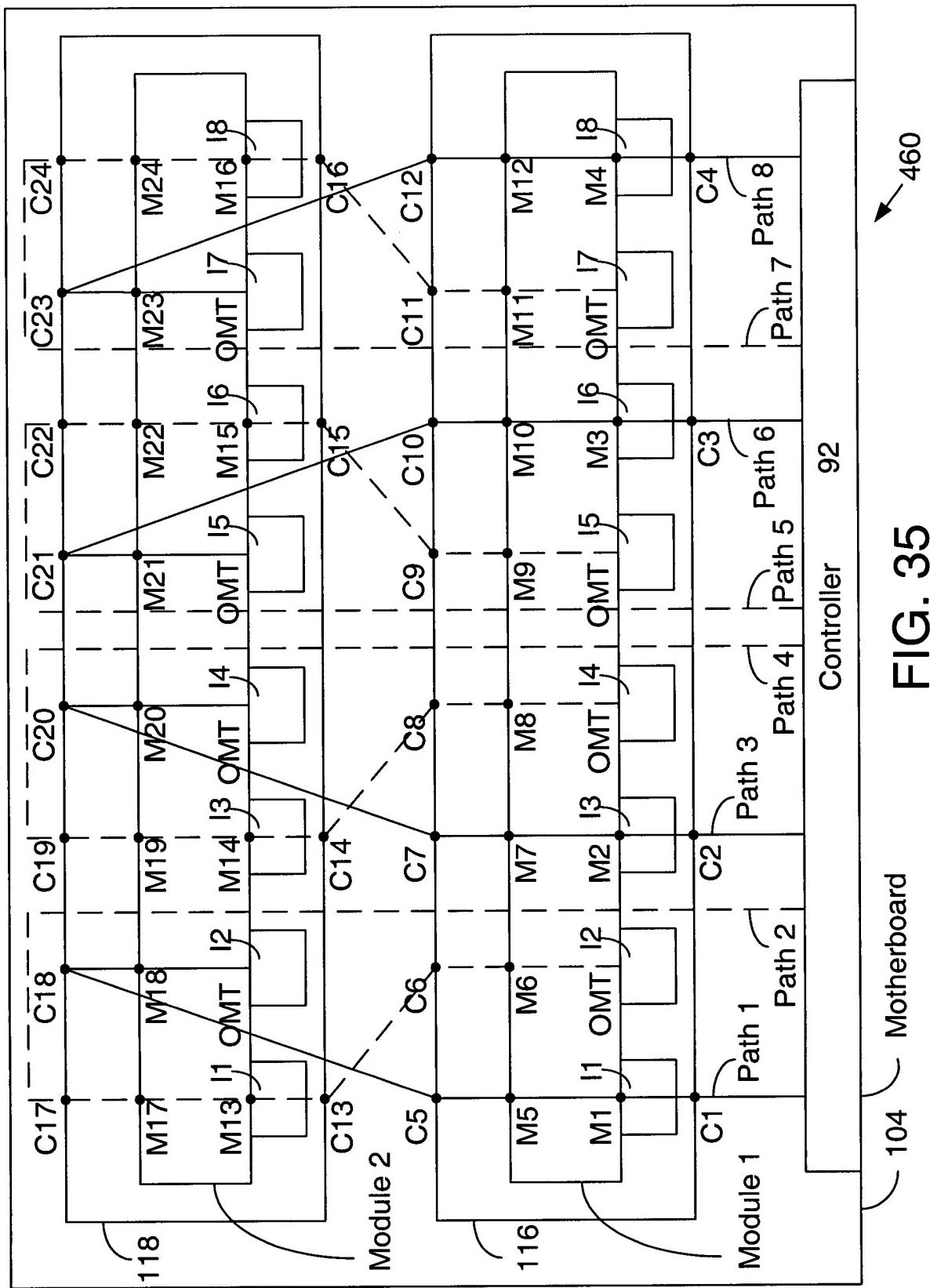


FIG. 35

104 Motherboard

92 Controller

